

region electrically connected to the signal wiring, and a pixel electrode to which the other one of the source region and the drain region of the thin film transistor is electrically connected via a lead electrode, wherein

the signal wiring, the gate wiring, the auxiliary capacitance wiring and the lead electrode are made of light shading material(s),

a semiconductor thin film is formed for a pixel electrode below and so as to be entirely covered by respective parts of the signal wiring, the gate wiring, the auxiliary capacitance wiring and the lead electrode,

a region of the semiconductor thin film located below and entirely covered by the signal wiring and below and at least partially covered by the gate wiring serves as a channel region of the thin film transistor, regions of the semiconductor thin film located on both sides of the channel region below the signal wiring serve as a source region and a drain region of the thin film transistor respectively, and a region of the semiconductor thin film located below the auxiliary capacitance wiring serves as an auxiliary capacitance electrode region.

5. (Amended) A transmission type liquid crystal display device as claimed in claim 1, further comprising:

a first contact hole for electrically connecting either one of the source region and the drain region of the semiconductor thin film to the signal wiring, a second contact hole for electrically connecting to the lead electrode an auxiliary capacitance electrode region lead to the other one of the source region and the drain region of the semiconductor thin

film, and a third contact hole for electrically connecting the lead electrode to the pixel electrode.

6. (Amended) A transmission type liquid crystal display device as claimed in claim 1, wherein a gate electrode of the transistor and the auxiliary capacitance wiring are made of the same material.

Please add the following new claims:

9. (New) A liquid crystal display device which uses at least transmissive light to display images, the liquid crystal display comprising:

a gate wiring, a signal wiring perpendicular to the gate wiring, an auxiliary capacitance wiring that is generally parallel to the gate wiring and perpendicular to the signal wiring, a thin film transistor having either one of a source region and a drain region electrically connected to the signal wiring, and a pixel electrode to which the other one of the source region and the drain region of the thin film transistor is electrically connected via a lead electrode;

wherein the signal wiring, the gate wiring, the auxiliary capacitance wiring and the lead electrode are opaque so as to shade light; and

wherein a region of a semiconductor film for the transistor that is entirely covered by the signal wiring and which is at least partially covered by the gate wiring serves as at least part of a channel region of the thin film transistor.